

**Amendments to the Claims**

These claims will replace all prior versions, and listings, of claims in the application:

1. (canceled)

2. (currently amended) A decision feedback equalizer (DFE), comprising:

~~a forward equalizer;~~  
~~first and second adders;~~  
~~a decision device;~~  
~~a feedback equalizer; and~~  
~~an N-tap filter;~~

wherein:

~~the a first adder, and a second adders adder, the a decision device and the a feedback~~  
~~equalizer constitute arranged to form a first feedback loop;~~

~~the second adder, the decision device, and the N-tap a filter arranged to form constitute a~~  
~~second feedback loop; the filter being in parallel with the feedback equalizer to compensate for a~~  
~~delay introduced by the feedback equalizer such that the second feedback loop is free of an~~  
~~implementation the delay associated with the first feedback loop; wherein~~

~~N is a positive integer;~~

~~and the an output signal of the decision feedback equalizer DFE is common with an input~~  
~~signal of the decision device.~~

3. (currently amended) The DFE as recited in claim 2, wherein the ~~N-tap~~ filter is implemented in

fast logic.

4. (currently amended) ~~A digital television receiver including the~~ The DFE as recited in claim  
2, wherein the DFE is included in a digital television receiver.

Claims 5 - 9 (Canceled)

10. (canceled)

11. (canceled)

12. (canceled)

13. (new) A method for controlling a decision feedback equalizer (DFE), comprising:

arranging a first adder, a second adder, a decision device and a feedback equalizer to  
form a first feedback loop; and

arranging the second adder, the decision device, and a filter to form a second feedback  
loop, the filter being in parallel with the feedback equalizer to compensate for a delay introduced  
by the feedback equalizer such that the second feedback loop is free of the delay associated with  
the first feedback loop, wherein an output signal of the DFE is common with an input signal of  
the decision device.